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Re: Serial No.: 09/170,625
Applicant(s): Duck-Kyon CHOI
Filing Date: October 13, 1998
For: METHOD FOR FABRICATING A THIN FILM TRANSISTOR
Group Art Unit: 2823
Examiner: S. Hawranek

SIR:

Attached hereto for filing are the following papers:

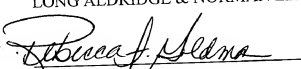
IDS
PTO-1449
14 References
Related Cases Statement

Our check in the amount of \$ 240.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. §1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 50-0911. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. §1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

LONG ALDRIDGE & NORMAN LLP

Date: October 5, 2000


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